



# THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

Approved by NTE, Govt. of Maharashtra & Affiliated to University of Mumbai  
Institute Accredited by National Assessment and Accreditation Council (NAAC), Bangalore  
Recognized by Government of Maharashtra, Mumbai, Maharashtra  
Approved by Government of Maharashtra, Mumbai, Maharashtra  
Approved by Government of Maharashtra, Mumbai, Maharashtra

A. Block - Thakur College of Engineering & Technology  
Thakur Village, Thakur Road, Kandivli (E), Mumbai - 400 101  
Tel : 022 2572 7227-7232  
Fax : 2572 3025  
E-mail : thakurcollege@rediffmail.com  
Website : www.thakurcollege.edu.in



REPORT DATE : 05/01/2018

OFFICE REGISTER FOR SECOND YEAR SEMESTER III (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

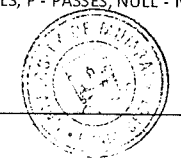
Seat No.	Name	THEORY	----- COURSE-I -----		----- COURSE-II -----		----- COURSE-III -----		----- COURSE-IV -----		----- COURSE-V -----		ΣC	ΣCG	GPA	Result
			80/32	20/08	100 C G GP C*GP	80/32	20/08	100 C G GP C*GP	80/32	20/08	100 C G GP C*GP	80/32				

COURSE I : APPLIED MATHEMATICS III  
 COURSE II : ELECTRONIC DEVICES  
 COURSE III : DIGITAL CIRCUITS AND DESIGN  
 COURSE IV : CIRCUIT THEORY  
 COURSE V : ELECTRONIC INSTRUMENTS AND MEASUREMENTS  
 COURSE VI : CIRCUIT THEORY AND MEASUREMENT LABORATORY  
 COURSE VII : OBJECT ORIENTED PROGRAMMING LABORATORY

Seat No.	Name	6F	F	8+	P	-	-	-	-	-	32+	P	8+	P	40	4	P	4	16	3F	F	8+	P	-	-	-	-	-	AB	F	8+	P	-	-	-	-	32+	P	8+	P	40	4	P	4	16	ΣC	ΣCG	GPA	Result	
34001	ARORA NIRMIT HARBANS ALKA	17+C	1	7	7						14+	D	25+	D	39	1	D	6	6	21+	O	28+	D	49	1	C	7	7	12+	E	1	5	5					20+	O	30+	C	50	2	C	7	14	14	71	-	F
34002	/DUBEY RAJ SANTOSH RITA	18+B	1	8	8						17+	C	20+	P	37	1	E	5	5	20+	O	20+	P	40	1	D	6	6	17+	C	1	7	7					42+	D	9+	E	51	4	D	6	24	26	140	5.38	P
34003	/GAIKWAD PRIYANKA SURESH SUKHADA	17+C	1	7	7						21+	O	36+	B	57	1	A	9	9	21+	O	33+	C	54	1	B	8	8	16+	C	1	7	7					45+	D	16+	O	61	4	C	7	28	26	163	6.27	P
34004	GUPTA SAURABH SANTOSH SANDHYA	18+B	1	8	8						16+	C	27+	D	43	1	D	6	6	21+	O	46+	O	67	1	O	10	10	12+	E	1	5	5					47+	D	9+	E	56	4	D	6	24	26	137	5.27	P
34005	GUPTA SUNILKUMAR PHOOLCHAND VINITA	19+A	1	9	9						22+	O	28+	D	50	1	C	7	7	21+	O	34+	C	55	1	B	8	8	21+	O	1	10	10					45+	D	13+	C	58	4	D	6	24	26	158	6.08	P
34006	INGLE MANDAR SUSHIL SONIA	17+C	1	7	7						14+	D	20+	P	34	1	E	5	5	17+	C	20+	P	37	1	E	5	5	17+	C	1	7	7					54+	C	9+	E	63	4	C	7	28	26	140	5.38	P
34007	/JHA ANKITA SHYAMANAD SEEMA	17+C	1	7	7						12+	E	28+	D	40	1	D	6	6	19+	A	36+	B	55	1	B	8	8	18+	B	1	8	8					36+	E	8+	P	44	4	P	4	16	26	137	5.27	P
34008	NERKAR VISHAL RAJENDRA SUREKHA	17+C	1	7	7						18+	B	34+	C	52	1	C	7	7	23+	O	33+	C	56	1	B	8	8	20+	O	1	10	10					61+	A	8+	P	69	4	C	7	28	26	160	6.15	P
34009	SANADI RISHABH ATULKUMAR SMITA	16+C	1	7	7						13+	D	22+	P	35	1	E	5	5	19+	A	21+	P	40	1	D	6	6	11+	P	1	4	4					37+	E	8+	P	45	4	E	5	20	18	92	-	F

/ - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID  
 G : GRADE GP : GRADE POINTS C : CREDITS CP : CREDIT POINTS ΣC : SUM OF PRODUCT OF CREDITS & GRADES ΣCG : SUM OF CREDIT POINTS GPA : ΣCG / ΣC  
 MARKS : >=80 >=75 AND <80 >=70 AND <75 >=60 AND <70 >=50 AND <60 >=45 AND <50 >=40 AND <45 <40  
 GRADE : 0 A B C D E P F  
 GRADE POINT : 10 9 8 7 6 5 4 0

PREPARED BY: [Signature]  
 READ BY: [Signature]  
 CHECKED BY: [Signature]  
 IN-CHARGE EXAMINATIONS: [Signature]  
 PRINCIPAL: [Signature]  
 THAKUR COLLEGE OF ENG. & TECH  
 Shyam Narayan Road, Thakur Village,  
 Kandivli (E), Mumbai-400 101.





THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

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REPORT DATE : 05/01/2018

OFFICE REGISTER FOR SECOND YEAR SEMESTER III (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

Table with columns: Seat No., Name, THEORY, COURSE-I, COURSE-II, COURSE-III, COURSE-IV, COURSE-V, COURSE-VI, COURSE-VII, ΣC, ΣCG, GPA, Result.

COURSE I : APPLIED MATHEMATICS III

COURSE II : ELECTRONIC DEVICES

COURSE III : DIGITAL CIRCUITS AND DESIGN

COURSE IV : CIRCUIT THEORY

COURSE V : ELECTRONIC INSTRUMENTS AND MEASUREMENTS

COURSE VI : CIRCUIT THEORY AND MEASUREMENT LABORATORY

COURSE VII : OBJECT ORIENTED PROGRAMMING LABORATORY

Main student results table with columns for student ID, name, theory marks, practical marks, and final GPA/Result.

Legend for marks and grades: / - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID.

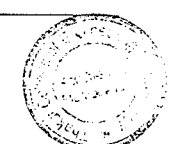
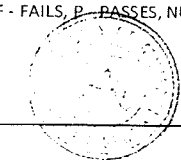
PREPARED BY

READ BY

CHECKED BY

IN-CHARGE EXAMINATIONS

PRINCIPAL THAKUR COLLEGE OF ENG. & TECH Shyam Narayan Road, Thakur Village, Kandivli (E), Mumbai-400 101.





**THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY**  
 Applied in Maharashtra & Affiliated to University of Mumbai  
 Institute Accredited by National Assessment and Accreditation Council (NAAC)  
 Performance of students by their own Board of Examinations. Please Refer

Approved by Maharashtra State Board of Technical Education  
 Maharashtra State Board of Technical Education  
 Mumbai - 400 001  
 Phone : 2211 3111  
 Fax : 2211 3111  
 Website : www.mhacet.edu



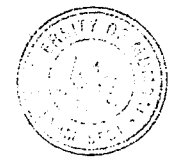
REPORT DATE : 05/01/2018

OFFICE REGISTER FOR SECOND YEAR SEMESTER III (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

Seat No.	Name	THEORY	-----< COURSE-I ----->		-----< COURSE-II ----->		-----< COURSE-III ----->		-----< COURSE-IV ----->		-----< COURSE-V ----->		ΣC	ΣCG	GPA	Result																		
			80/32	20/08	100	C	G	GP	C*GP	80/32	20/08	100					C	G	GP	C*GP	80/32	20/08	100	C	G	GP	C*GP	80/32	20/08	100	C	G	GP	C*GP
		TW/OR/PR	-----< COURSE-I ----->		-----< COURSE-II ----->		-----< COURSE-III ----->		-----< COURSE-IV ----->		-----< COURSE-V ----->																							
			25/10	C	GP	C*GP	25/10	50/20	C	G	GP	C*GP	25/10	50/20	C	G	GP	C*GP	25/10	C	GP	C*GP	25/10	50/20	C	G	GP	C*GP	25/10	50/20	C	G	GP	C*GP

- COURSE I : APPLIED MATHEMATICS III
- COURSE II : ELECTRONIC DEVICES
- COURSE III : DIGITAL CIRCUITS AND DESIGN
- COURSE IV : CIRCUIT THEORY
- COURSE V : ELECTRONIC INSTRUMENTS AND MEASUREMENTS
- COURSE VI : CIRCUIT THEORY AND MEASUREMENT LABORATORY
- COURSE VII : OBJECT ORIENTED PROGRAMMING LABORATORY

34019	TIWARI SACHIN PRABHAKAR MITHILESH	21F F 8+ P - - - - - 19+A 1 9 9	32+ P 10+ D 42 4 P 4 16 19+ A 26+ D 45 1 C 7 7	35+ P 8+ P 43 4 P 4 16 19+ A 21+P 40 1 D 6 6	35+P 14+B 49 4 E 5 20 22+O 1 10 10	52+ C 13+C 65 4 C 7 28 21+ O 32+C 53 2 B 8 16	22	128	-	F
34020	/BIYALA SHRUTI SUSHIL RENU	13F F 8+ P - - - - - 18+B 1 8 8	32+ P 11+ D 43 4 P 4 16 21+ O 38+ A 59 1 A 9 9	32E P 8+ P 40 4 P 4 16 23+ O 24+E 47 1 C 7 7	32+P 18+O 50 4 D 6 24 21+O 1 10 10	34+ P 12+C 46 4 E 5 20 17+ C 42+O 59 2 A 9 18	22	128	-	F



*Sachin*  
 Assistant Registrar  
 (Examination)  
 Date: 22/1/18

/ - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID  
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 MARKS : >=80 >=75 AND <80 >=70 AND <75 >=60 AND <70 >=50 AND <60 >=45 AND <50 >=40 AND <45 <40  
 GRADE : 0 A B C D E P F  
 GRADE POINT : 10 9 8 7 6 5 4 0

PREPARED BY

READ BY

CHECKED BY

IN-CHARGE EXAMINATIONS

PRINCIPAL  
 THAKUR COLLEGE OF ENGG. & TECH.  
 Shyam Narayan Road, Thakur Village  
 Kandivli (E), Mumbai-400 101.





THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

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MAKER TRUSTS, Thakur College of Engineering & Technology, Shyam Narayan Road, Thakur Village, Kandivli (E), Mumbai - 400 101. Phone: 2254 3100, 2254 3101, 2254 3102, 2254 3103, 2254 3104, 2254 3105, 2254 3106, 2254 3107, 2254 3108, 2254 3109, 2254 3110, 2254 3111, 2254 3112, 2254 3113, 2254 3114, 2254 3115, 2254 3116, 2254 3117, 2254 3118, 2254 3119, 2254 3120.



REPORT DATE : 05/01/2018

OFFICE REGISTER FOR SECOND YEAR SEMESTER IV (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

Table with columns: Seat No., Name, THEORY, COURSE-I, COURSE-II, COURSE-III, COURSE-IV, COURSE-V, COURSE-VI, COURSE-VII, COURSE-VIII, COURSE-IX, ΣC, ΣCG, GPA, Result.

- COURSE I : APPLIED MATHEMATICS IV
COURSE II : DISCRETE ELECTRONIC CIRCUITS
COURSE III : MICROPROCESSOR AND PERIPHERALS
COURSE IV : PRINCIPALS OF CONTROL SYSTEMS
COURSE V : FUNDAMENTALS OF COMMUNICATION ENGINEERING
COURSE VI : ELECTRICAL MACHINES
COURSE VII : DISCRETE ELECTRONICS LABORATORY
COURSE VIII : CONTROL SYSTEMS AND ELECTRICAL MACHINES LABORATORY
COURSE IX : COMMUNICATION ENGINEERING LABORATORY

Main examination results table with columns for student ID, name, theory marks, practical marks, and final GPA/Result.

Legend for marks: / - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID.
Grade Legend: GP : GRADE POINTS, C : CREDITS, CP : CREDIT POINTS, Σ : SUM OF PRODUCT OF CREDITS & GRADES, ΣC : SUM OF CREDIT POINTS, GPA : ΣCG / ΣC.

Signature lines for: PREPARED BY, READ BY, CHECKED BY, IN-CHARGE EXAMINATIONS.

Official stamps and signatures including the Principal's signature and the Thakur College of Engineering & Technology stamp.



THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

Approved by All India Council of Technical Education, Ministry of Education, Government of India. Affiliated to the Maharashtra State Board of Technical Education (MATSBE), Mumbai. Recognized by National Board of Accreditation (NBA), New Delhi.

1. B.Tech. in Electronics Engineering (Electronics Engineering) - 2015-16  
2. B.Tech. in Electronics Engineering (Electronics Engineering) - 2016-17  
3. B.Tech. in Electronics Engineering (Electronics Engineering) - 2017-18  
4. B.Tech. in Electronics Engineering (Electronics Engineering) - 2018-19  
5. B.Tech. in Electronics Engineering (Electronics Engineering) - 2019-20  
6. B.Tech. in Electronics Engineering (Electronics Engineering) - 2020-21  
7. B.Tech. in Electronics Engineering (Electronics Engineering) - 2021-22  
8. B.Tech. in Electronics Engineering (Electronics Engineering) - 2022-23  
9. B.Tech. in Electronics Engineering (Electronics Engineering) - 2023-24  
10. B.Tech. in Electronics Engineering (Electronics Engineering) - 2024-25



REPORT DATE : 05/01/2018

OFFICE REGISTER FOR SECOND YEAR SEMESTER IV (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

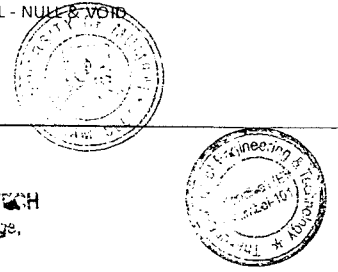
Table with columns: Seat No., Name, THEORY, COURSE-I, COURSE-II, COURSE-III, COURSE-IV, COURSE-V, COURSE-VI, ΣC, ΣCG, GPA, Result. Includes rows for TW/OR/PR and course details.

- COURSE I : APPLIED MATHEMATICS IV
COURSE II : DISCRETE ELECTRONIC CIRCUITS
COURSE III : MICROPROCESSOR AND PERIPHERALS
COURSE IV : PRINCIPALS OF CONTROL SYSTEMS
COURSE V : FUNDAMENTALS OF COMMUNICATION ENGINEERING
COURSE VI : ELECTRICAL MACHINES
COURSE VII : DISCRETE ELECTRONICS LABORATORY
COURSE VIII : CONTROL SYSTEMS AND ELECTRICAL MACHINES LABORATORY
COURSE IX : COMMUNICATION ENGINEERING LABORATORY

Main examination results table with columns for student ID, name, and performance metrics across various courses (I-IX).

/ - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID
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MARKS : >=80 >=75 AND <80 >=70 AND <75 >=60 AND <70 >=50 AND <60 >=45 AND <50 >=40 AND <45 <40
GRADE : O A B C D E P F
GRADE POINT : 10 9 8 7 6 5 4 0

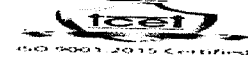
PREPARED BY, READ BY, CHECKED BY, IN-CHARGE EXAMINATIONS, PRINCIPAL, THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY, Shyam Narayan Road, Thakur Village, Kandivli (E), Mumbai-400 101.





THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

Approved by the Maharashtra State Board of Technical Education, Mumbai. Approved by the Maharashtra State Board of Technical Education, Mumbai. Approved by the Maharashtra State Board of Technical Education, Mumbai.



REPORT DATE : 05/01/2018 OFFICE REGISTER FOR SECOND YEAR SEMESTER IV (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

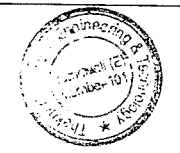
Table with columns: Seat No., Name, THEORY, COURSE-I, COURSE-II, COURSE-III, COURSE-IV, COURSE-V, COURSE-VI, TW/OR/PR, COURSE-VII, COURSE-III, COURSE-VIII, COURSE-IX, and Result.

COURSE I : APPLIED MATHEMATICS IV COURSE II : DISCRETE ELECTRONIC CIRCUITS COURSE III : MICROPROCESSOR AND PERIPHERALS COURSE IV : PRINCIPALS OF CONTROL SYSTEMS COURSE V : FUNDAMENTALS OF COMMUNICATION ENGINEERING COURSE VI : ELECTRICAL MACHINES COURSE VII : DISCRETE ELECTRONICS LABORATORY COURSE VIII : CONTROL SYSTEMS AND ELECTRICAL MACHINES LABORATORY COURSE IX : COMMUNICATION ENGINEERING LABORATORY

Main student performance table with columns for Seat No., Name, and scores for various courses (I-IX) and final GPA/Result.

Legend for marks and grades: / - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID.

Signature blocks for: PREPARED BY, READ BY, CHECKED BY, IN-CHARGE EXAMINATIONS, Assistant Registrar (Examinations), and PRINCIPAL THAKUR COLLEGE OF ENG. & TECH.





REPORT DATE : 05/01/2018

OFFICE REGISTER FOR THIRD YEAR SEMESTER VI (CREDIT BASED GRADING SYSTEM) (BRANCH - ELECTRONICS ENGINEERING) EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

Table with columns: Seat No., Name, THEORY, COURSE-I, COURSE-II, COURSE-III, COURSE-IV, COURSE-V, COURSE-VI, COURSE-VII, COURSE-VIII, ΣC, ΣCG, GPA, Result

- COURSE I : BASIC VLSI DESIGN
COURSE II : ADVANCED INSTRUMENTATION SYSTEMS
COURSE III : COMPUTER ORGANIZATION
COURSE IV : POWER ELECTRONICS-I
COURSE V : DIGITAL SIGNAL PROCESSING AND PROCESSORS
COURSE VI : MODERN INFORMATION TECHNOLOGY FOR MANAGEMENT
COURSE VII : ADVANCE INSTRUMENTATION AND POWER ELECTRONICS LABORATORY
COURSE VIII : MINI PROJECT-II

Main student performance table with columns for student ID, name, and grades for each course (I-VIII) and final GPA/Result.

/ - FEMALE, # - O.229, @ - O.5042, \* - O.5045, R - 8566, ADC - ADMISSION CANCELLED, RR - RESERVED, - : FAILS IN THEORY OR ORAL OR PRACTICAL, RPV - PROVISIONAL, RCC - O.5050, A ABS - ABSENT, F - FAILS, P - PASSES, NULL - NULL & VOID
G : GRADE GP : GRADE POINTS C : CREDITS CP : CREDIT POINTS ΣCG : SUM OF PRODUCT OF CREDITS & GRADES ΣC : SUM OF CREDIT POINTS GPA : ΣCG / ΣC

PREPARED BY, READ BY, CHECKED BY, IN-CHARGE EXAMINATIONS, PRINCIPAL, THAKUR COLLEGE OF ENG. & TECH, Shyam Narayan Road, Thakur Village, Kandivli (E), Mumbai-400 101.



# THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY

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Programmes Accredited by National Board of Examinations (NBE), New Delhi

Address: Thakur College of Engineering & Technology  
Shyam Narayan Road, Thakur Village,  
Kandivli (E) Mumbai - 400 101  
Tel: 022-25789271, 25789272  
Fax: 25789273  
E-mail: info@thakurcollege.edu.in  
Website: www.thakurcollege.edu.in



REPORT DATE : 06/01/2018.

OFFICE REGISTER FOR SECOND YEAR SEMESTER III (REVISED TO CBSGS) [BRANCH - ELECTRONICS ENGINEERING] EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

SEAT NO.	NAME OF THE CANDIDATE	PP1			PP2				PP3				PP4		PP5		PP6		PP7		PP8	SEM III TOTAL	RESUL
		TH	IA	TW	TH	IA	TW	P&O	TH	IA	TW	P&O	TH	IA	TH	TW	TW	TW	P&O	TH			
	MAX	80	20	25	80	20	25	50	80	80	25	50	80	20	80	20	25	25	50	80			
	MIN	32	8	10	32	8	10	20	32	8	10	20	32	8	32	8	10	10	20	32			

PP1	PP2	PP3	PP4	PP5	PP6	PP7	PP8
APPLIED MATHEMATICS III	ELECTRONIC DEVICES	DIGITAL CIRCUITS AND DESIGN	CIRCUIT THEORY	ELECTRONIC INSTRUMENTS AND MEASUREMENTS	CIRCUIT THEORY AND MEASUREMENT LABORATORY	OBJECT ORIENTED PROGRAMMING LABORATORY	PRINCIPLES OF CONTROL SYSTEM (SEM IV)

39001	YADAV DEVENDRA KUMAR VANSH BHADUR RITA	--	--	--	44	--	EX	EX	--	--	--	--	--	--	--	--	--	--	--	59	103/160	P
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NOTE: Circular No. UG/244 of 2017-2018 dated 17th October, 2017



*[Signature]*  
Assistant Registrar  
(Examination)  
20/1/18

#:O.229;@:O.5042,O.5043;\*:O.5045;+:MARKS CARRIED; /:FEMALE; AA:ABSENT; --:NOT APPLICABLE; P:PASS CLASS; F:FAIL; EX:EXMP CAN BE CLAIMED; NULL:NULL & VOID AS NO MARKS IN T.W.; RCC:O.5050; RR:RESERVED; ADC:ADMN CANCELLED; RLE:LOWER EXAM NOT CLEARED; PPR:PASSED PREVIOUSLY

*[Signature]*  
PREPARED BY

*[Signature]*  
READ BY

*[Signature]*  
CHECKED BY

*[Signature]*  
IN-CHARGE EXAMINATIONS

*[Signature]*  
PRINCIPAL  
THAKUR COLLEGE OF ENG. & T.  
Shyam Narayan Road, Thakur Village,  
Kandivli (E), Mumbai-400 101.







**THAKUR COLLEGE OF ENGINEERING & TECHNOLOGY**

Approved by AICTE, Govt. of Maharashtra & Affiliated to University of Mumbai  
 Institute Accredited by National Assessment and Accreditation Council (NAAC), Bangalore  
 Programmes Accredited by National Board of Accreditation (NBA), New Delhi  
 Programmes Accredited by Programmes:  
 1. Computer Engineering (Sem I, II, III, IV)  
 2. Electronics Engineering (Sem I, II, III, IV)  
 3. Information & Communication Engineering (Sem I, II, III, IV)  
 4. Computer Engineering (Sem I, II, III, IV)  
 5. Electronics Engineering (Sem I, II, III, IV)  
 6. Information & Communication Engineering (Sem I, II, III, IV)

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REPORT DATE : 05/01/2018.

OFFICE REGISTER FOR SECOND YEAR SEMESTER IV (REVISED TO CBSGS) [BRANCH - ELECTRONICS ENGINEERING] EXAMINATION HELD IN DECEMBER 2017 HELD BY THE COLLEGE ON BEHALF OF THE UNIVERSITY

SEAT NO.	NAME OF THE CANDIDATE	PP1			PP2			PP3			PP4		PP5		PP6		PP7		PP8		PP9		SEM IV RESULT TOTAL
		TH	IA	TW	TH	IA	TW	P&O	TH	IA	TW	P&O	TH	IA	TH	IA	TH	IA	TW	P&O	TW	P&O	
	MAX	80	20	25	80	20	25	50	80	20	25	25	80	20	80	20	60	15	25	25	25	25	80
	MIN	32	8	10	32	8	10	20	32	8	10	10	32	8	32	8	24	6	10	10	10	10	32

PP1	PP2	PP3	PP4	PP5	PP6	PP7	PP8	PP9
APPLIED MATHEMATICS IV	DISCRETE ELECTRONIC CIRCUITS	MICROPROCESSOR AND PERIPHERALS	PRINCIPLES OF CONTROL SYSTEMS	FUNDAMENTALS OF COMMUNICATION ENGINEERING	ELECTRICAL MACHINES	CONTROL SYSTEMS AND ELECTRICAL MACHINES LABORATORY	COMMUNICATION ENGINEERING LABORATORY	ASIC VERIFICATION (SEM VII)

49001	YADAV DEVENDRA KUMAR VANSI BHADUR RITA	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	19F	19/80	F
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NOTE: Circular No. UG/244 of 2017-2018 dated 17th October, 2017



*Chait*  
 Assistant Registrar  
 (Examination)  
 05/01/18

#:O.229;@:O.5042,O.5043;\*:O.5045;+:MARKS CARRIED; /:FEMALE; AA:ABSENT; --:NOT APPLICABLE; P:PASS CLASS; F:FAIL; EX:EXMP CAN BE CLAIMED; NULL:NULL & VOID AS NO MARKS IN T.W.; RCC:O.5050; RR:RESERVED; ADC:ADMN CANCELLED; RLE:LOWER EXAM NOT CLEARED; PPR:PASSED PREVIOUSLY

*M. Kulkarni*  
 PREPARED BY

*read*  
 READ BY

*Soni*  
 CHECKED BY

*K. Somale*  
 IN-CHARGE EXAMINATIONS

*M. Kulkarni*  
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