



REPORT DATE : 10/01/2020.

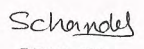
PROVISIONAL OFFICE REGISTER FOR THIRD YEAR SEMESTER V (REVISED TO CBSGS) [BRANCH - ELECTRONICS AND TELECOMMUNICATION ENGINEERING] EXAMINATION HELD IN DECEMBER 2019

SEAT NO.	NAME OF THE CANDIDATE	PP1				PP2				PP3			PP4		PP5		PP6			PP7		PP8		PP9		PP10		PP11		SEM V TOTAL	RESULT
		TH	IA	TW	P&O	TH	IA	TH	IA	TW	TH	IA	TH	IA	TW	TW	P&O	TW	P&O	TW	P&O	TW	P&O	TH	TH	TH	TH				
		MAX	80	20	25	25	80	20	80	20	25	80	20	80	20	80	20	50	25	25	25	25	25	25	25	25	80	80			
		MIN	32	8	10	10	32	8	32	8	10	32	8	32	8	20	10	10	10	10	10	10	10	10	10	32	32				
PP1	PP2	PP3	PP4				PP5			PP6		PP7			PP8		PP9		PP10		PP11										
MICROPROCESSORS AND APPLICATIONS	ANALOG COMMUNICATION	RANDOM SIGNAL ANALYSIS	RF MODELING AND ANTENNAS				INTEGRATED CIRCUITS			BUSINESS COMMUNICATION AND ETHICS		COMMUNICATION ENGINEERING LABORATORY I			COMMUNICATION ENGINEERING LABORATORY II		MINI PROJECT I		MICROPROCESSORS & PERIPHERALS (SEM IV)		SIGNALS AND SYSTEMS (SEM IV)										
57001	BAJAJ RIYA RAJESH KALPANA	--	--	--	--	--	--	32+	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	--	17F	AA	49/160	F		

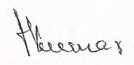
NOTE: Circular No. UG/244 of 2017-2018 dated 17th October, 2017

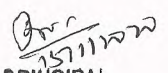
#:0.229;@:0.5042,0.5043;*:0.5045;+:MARKS CARRIED; /:FEMALE; AA:ABSENT; --:NOT APPLICABLE; P:PASS CLASS: F:FAIL; EX:EXMP CAN BE CLAIMED; NULL:NULL & VOID AS NO MARKS IN T.W.; RCC:0.5050; RR:RESERVED; ADC:ADMN CANCELLED; RLE:LOWER EXAM NOT CLEARED; PPR:PASSED PREVIOUSLY

PREPARED BY 

READ BY 

CHECKED BY 

CONTROLLER OF EXAMINATIONS 

PRINCIPAL 
 THAKUR COLLEGE OF ENG. & TECH
 Shyam Narayan Road, Thakur Village,
 Kandivli (E), Mumbai-400 101.

