

UNIVERSITY OF MUMBAI  
OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED COURSE) EXAMINATION HELD IN MAY 2017  
COLLEGE/CENTRE : 537 TCET AUGUST 2, 2017

SEAT NO.	NAME OF CANDIDATE CENTRE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. VLSI DESIGN  
03. POWER ELECTRONICS AND DRIVES  
05. ELECTIVE-I : 5:WIRELESS COMMUNICATION

02. FILTER DESIGN  
04. COMMUNICATION NETWORKS  
06. PROJECT-A

50170040	/JALNDRA PRATIKSHA OMPRAKA SH PARMESHWARI	55	16+	15+	44+	16+	18+	66+	17+	18+	52+	17+	20+	41+	16+	17+	19+	18+	465	P
50170041	SINGH VIVEK AWADHESHKUMAR VIMLA	AA	17+	18+	40+	18+	17+	49+	17+	16+	AA	12+	22+	55+	23+	20+	22+	22+	368	A ABS

05. ELECTIVE-I : 8:DIGITAL IMAGE PROCESSING DESIGN

50170042	/AMIN DEVIKA DEVENDRA MAYA	48	17+	11+	40+	17+	15+	40+	11+	15+	47+	12+	12+	41+	21+	16+	22+	20+	405	P
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#:0.229; @:0.5042; \*:0.5045;RCC:0.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
~ : DYSLEXIA BENEFIT