

## UNIVERSITY OF MUMBAI

OFFICE REGISTER FOR THE B.E. ( ELECTRONICS ENGINEERING) (SEM VII) (REVISED TO CBGS COURSE) EXAMINATION HELD IN NOVEMBER 2018  
 COLLEGE/CENTRE : 537 TCET MARCH 18, 2019

SEAT NO.	NAME OF CANDIDATE CENTRE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		

01. BASIC VLSI DESIGN (SEM-6)

03. POWER ELECTRONICS AND DRIVES

05. ELECTIVE-I : 35:WIRELESS COMMUNICATION

02. FILTER DESIGN

04. COMPUTER COMMUNICATION NETWORKS

06. PROJECT-A

11177015	SINGH VIVEK AWADHESHKUMAR VIMLA	AA	17+	18+	40+	18+	17+	49+	17+	16+	AA	12+	22+	55+	23+	20+	22+	22+	368	A ABS
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#:O.229; @:O.5042; \*:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;  
 --:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;  
 ~ : DYSLEXIA BENEFIT